

**DR. A.P.J. ABDUL KALAM TECHNICAL UNIVERSITY**  
**UTTAR PRADESH, LUCKNOW**



## **Syllabus**

**3<sup>rd</sup> Year**

**[Effective from Session 2016-17]**

- 1. B.Tech. Electronics Engineering**
- 2. B.Tech. Electronics & Communication Engineering**
- 3. B.Tech. Electronics & Telecommunication Engineering**

## SEMESTER-V

No.	Subject Code	Name of the Subject	Periods			Evaluation Scheme				Subject Total	Credit
			L	T	P	Sessional Assessment			ESE		
						CT	TA	Total			
<b>THEORY SUBJECTS</b>											
1	NEC 501R	Integrated Circuits	3	1	0	30	20	50	100	150	4
2	NEC 502	Principles of Communication	3	1	0	30	20	50	100	150	4
3	NEC 503	Microprocessors	3	1	0	30	20	50	100	150	4
4	NIC 501	Control System -I	3	1	0	30	20	50	100	150	4
5	NEC 504	Antenna and Wave Propagation	2	1	0	15	10	25	50	75	3
6	NHU501	Engineering Economics	2	0	0	15	10	25	50	75	2
<b>PRACTICAL/ DESIGN/ DRAWING</b>											
7	NEC 551R	Integrated Circuits Lab	0	0	2	10	10	20	30	50	1
8	NIC 551	Control System Lab	0	0	2	10	10	20	30	50	1
9	NEC 552	Communication Lab - 1	0	0	2	10	10	20	30	50	1
10	NEC 553	Microprocessors Lab	0	0	2	10	10	20	30	50	1
11	NGP 501	GP						50		50	
		<b>TOTAL</b>	16	5	8					1000	25

**CT** Class Test

**AT** Attendance

**TA** Tutorial Assignment

**ESE** End Semester Examination

**L/T/P** Lecture/ Tutorial/ Practical

## SEMESTER-VI

No.	Subject Code	Name of the Subject	Periods			Evaluation Scheme				Subject Total	Credit
			L	T	P	Sessional Assessment			ESE		
						CT	TA	Total			
<b>THEORY SUBJECTS</b>											
1	NEC 601	Microwave Engineering	3	1	0	30	20	50	100	150	4
2	NEC 602	Digital Communication	3	1	0	30	20	50	100	150	4
3	NEC 603	Integrated Circuit Technology	3	1	0	30	20	50	100	150	4
4	NEC 0_	Departmental Elective -I	3	1	0	30	20	50	100	150	4
5	NEC 0_	Departmental Elective - II	2	1	0	15	10	25	50	75	3
6	NHU601	Industrial Management	2	0	0	15	10	25	50	75	2
<b>PRACTICAL/ DESIGN/ DRAWING</b>											
7	NEC 651	Antenna and Microwave Lab	0	0	2	10	10	20	30	50	1
8	NEC 652	Communication Lab - II	0	0	2	10	10	20	30	50	1
9	NEC 653	CAD of Electronics Lab	0	0	2	10	10	20	30	50	1
10	NEC 654R	Seminar	0	0	1	10	10	20	-	20	1
11	NEC 655	Microcontrollers for Embedded Systems Lab	0	0	1	6	6	12	18	30	1
12	NGP 601	GP						50		50	
		<b>TOTAL</b>	16	5	8					1000	26

### Departmental Elective –I

- |            |  |
|------------|--|
| 1. NEC011  | Digital Signal Processing              |
| 2. NEC 012 | Computer Architecture and Organization |
| 3. NEC 013 | Artificial Neural Network              |
| 4. NEC 014 | Advance Semiconductor Devices          |
| 5. NEC013R | Real Time Systems                      |

### Departmental Elective - II

- |             |                                      |
|-------------|--------------------------------------|
| 1. NEC 021  | Industrial Electronics               |
| 2. NEC 022R | Microcontroller for Embedded Systems |
| 3. NEC 023  | Analog Signal Processing             |
| 4. NEC 024R | Advance Digital Design using Verilog |

NEC 501R Integrated Circuits		
Unit	Topic	Number of Lectures
I	<p><b>Analog Integrated circuit Design: an overview:</b> Current Mirrors using BJT and MOSFETs, Simple current Mirror, Base current compensated current Mirror, Wilson and Improved Wilson Current Mirrors, Widlar Current source and Cascode current Mirror</p> <p><b>The 741 IC Op-Amp:</b> Bias circuit, short circuit protection circuitry, the input stage, the second stage, the output stage, and device parameters; DC Analysis of 741: Small Signal Analysis of input stage, the second stage, the output stage; Gain, Frequency Response of 741; a Simplified Model, Slew Rate, Relationship Between <math>f_t</math> and SR</p>	10
II	<p><b>Linear Applications of IC op-amps:</b> An Overview of Op-Amp (ideal and non-ideal) based Circuits V-I and I-V converters, generalized Impedance converter, simulation of inductors</p> <p><b>Filters:</b> First and second order LP, HP, BP BS and All pass active filters, KHN.</p>	8
III	<p><b>Digital Integrated Circuit Design-An Overview:</b> CMOS Logic Gate Circuits: Basic Structure CMOS realization of Inverters, AND, OR, NAND and NOR Gates</p> <p><b>Latches and Flip flops:</b> The Latch, The SR Flip-flop, CMOS Implementation of SR Flip- flops, A Simpler CMOS Implementation of the Clocked SR Flip-flop, D Flip-flop Circuits.</p>	8
IV	<p><b>Non-Linear applications of IC Op-amps:</b> Log–Anti Log Amplifiers, Precision Rectifiers, Peak Detectors, Simple and Hold Circuits, Analog Multipliers and their applications. Op- amp as a comparator, Zero crossing detector, Schmitt Trigger, Astable multi vibrator, Mono stable multi vibrator, Generation of Triangular Waveforms</p>	7
V	<p><b>D/A and A/D converters</b></p> <p><b>Integrated Circuit Timer:</b> The 555 Circuit, Implementing a Monostable Multivibrator Using the 555 IC, Astable Multi vibrator Using the 555 IC.</p> <p><b>Phase locked loops (PLL):</b> Ex-OR Gates and multipliers as phase detectors, Block Diagram of IC PLL, Working of PLL and Applications of PLL.</p>	7

#### Text Books:

1. Sedra and Smith, “Microelectronic Circuits”, 6<sup>th</sup> Edition, Oxford University Press.
2. Michael Jacob, “Applications and Design with Analog Integrated Circuits”, PHI, 2<sup>nd</sup> Edition.

#### Reference Books:

1. Jacob Millman and Arvin Grabel, “Microelectronics”, 2nd Edition, Tata McGraw Hill.
2. Behzad Razavi, “Fundamentals of Microelectronics”, 2nd Edition, Wiley.
3. Mark N. Horenstein, “Microelectronic Circuits and Devices”, PHI.
4. Paul R. Gray, Paul J. Hurst, Stephen H. Lewis and Robert G. Meyer, “Analysis and Design of Analog Integrated Circuits”, Wiley.
5. Data Sheet: <http://www.ti.com/lit/ds/symlink/tl082.pdf>

6. Application Note: <http://www.ti.com/lit/an/sloa020a/sloa020a.pdf>
7. MPY634 Data Sheet: <http://www.ti.com/lit/ds/symlink/mpy634.pdf>
8. Application Note: <http://www.ti.com/lit/an/sbfa006/sbfa006.pdf>
9. ASLK Pro Manual: ASLK Manual

<b>NEC 502 Principles of Communication</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	Introduction: Overview of Communication system, Communication channels, Need for modulation, Baseband and Pass band signals, Amplitude Modulation: Double side bandwidth Carrier (DSB-C), Double side band without Carrier, Single Side Band Modulation, DSB-SC, DSB-C, SSB Modulators and Demodulators, Vestigial Side Band (VSB), Quadrature Amplitude Modulator, Radio Transmitter and Receiver.	10
II	Angle Modulation, Tone Modulated FM Signal, Arbitrary Modulated FM Signal, FM Modulators and Demodulators, Approximately Compatible SSB Systems, Stereophonic FM Broadcasting, Examples Based on Mat Lab.	8
III	Pulse Modulation, Digital Transmission of Analog Signals: Sampling Theorem and its applications, Pulse Amplitude Modulation (PAM), Pulse Width Modulation, Pulse Position Modulation. Their generation and Demodulation, Digital Representation of Analog Signals, Pulse Code Modulation (PCM), PCM System, Issues in digital transmission: Frequency Division Multiplexing, Time Division Multiplexing, Line Coding and their Power Spectral density, T1 Digital System, TDM Hierarchy.	8
IV	Differential Pulse Code Modulation, Delta Modulation. Adaptive Delta Modulation, Voice Coders, Sources of Noises, Frequency domain representation of Noise, Super position of Noises, Linear filtering of Noises, Mathematical Representation of Noise.	7
V	Noise in Amplitude Modulation: Analysis, Signal to Noise Ratio, Figure of Merit. Noise in Frequency Modulation: Pre-Emphasis, De-Emphasis and SNR Improvement, Phase Locked Loops: Analog and Digital.	7

**Text Book:**

1. Herbert Taub and Donald L. Schilling, "Principles of Communication Systems", Tata McGraw Hill Publication.

**Reference Books:**

1. B.P.Lathi, "Modern Digital and Analog Communication Systems", Oxford University Press.
2. Simon Haykin, "Communication Systems", Wiley India Publication.
3. H.P.Hsu & D.Mitra, "Analog and Digital Communications", Tata McGraw-Hill Publication.

<b>NEC 503 MICROPROCESSORS</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
<b>1.</b>	Evolution of microprocessors, Microprocessor architecture and its operations, 8085 pins description, programming model, basic interfacing concepts, input and output devices, logic devices and memory interfacing, addressing modes, Concept of instruction cycle, machine cycle and T-states, Concept of interrupts, Classification of 8085 instructions.	<b>8</b>
<b>2.</b>	8086 architecture-functional diagram, register organization, memory segmentation, programming model, memory address, physical memory organization, pins description, clock generator 8284A, maximum mode and minimum mode signal descriptions, timing diagrams, introduction to DOS and BIOS interrupts.	<b>8</b>
<b>3.</b>	Instruction formats, addressing modes, classification of instruction set, assembler directives (debug, TASM & MASM), macros, Programs techniques and assembly language programs: simple programs involves data transfer operation, arithmetic operation, logical operation, branch operation, machine control operation, string manipulations, stack and subroutine operations.	<b>8</b>
<b>4.</b>	8255 Programmable peripheral interfacing various mode of operation to 8086, interfacing keyboard and seven segment display, stepper motor interfacing, D/A and A/D converter, 8254 (8253) programmable interval timer, Direct Memory Access and 8237 DMA controller.	<b>8</b>
<b>5.</b>	Memory interfacing to 8086. Interrupt structure of 8086, interrupt handling, vector interrupt table and interrupt Service routine. Interfacing interrupts controller 8259 and DMA Controller 8257 to 8086. Serial communication standards, Serial data transfer schemes.	<b>8</b>

**Text Book:**

1. Ramesh Gaonkar, "Microprocessor architecture, programming and applications with the 8085", Penram International Publication (India) Pvt. Ltd.
2. Douglas V. Hall, "Microprocessors and Interfacing", Tata McGraw Hill Publication.

**Reference Books:**

1. Sivarama P. Dandamudi, "Introduction to Assembly Language Programming From 8086 to Pentium Processors", Springer Publication.
2. Walter A. Triebel and Avtar Singh, "The 8088 and 8086 Microprocessors: Programming, Interfacing Software, Hardware and Applications", Pearson Publication.
3. A. K. Ray and K. M. Bhurchandi, "Advance microprocessors and Peripherals" Tata McGraw Hill Publication.
4. Lyla B. Das, "The X86 Microprocessors, Architecture, Programming and Interfacing (8086 to Pentium)", Pearson Publication.

<b>NIC 501 Control System -I</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	Basic Components of a control system, Feedback and its effect, types of feedback control systems. Block diagrams Reduction and signal flow graphs, Modeling of Physical systems: electrical networks, mechanical systems elements, equations of mechanical systems, sensors and encoders in control systems, DC motors in control systems.	8
II	State-Variable Analysis: Vector matrix representation of state equation, state transition matrix, state-transition equation, relationship between state equations and high-order differential equations, relationship between state equations and transfer functions. Similarity Transformation, Decomposition of transfer functions, Controllability and observability.	8
in	Time domain Analysis of Control Systems: Time response of continuous data systems, typical test signals for the time response of control systems, the unit step response and time-domain specifications, Steady-State error, time response of a first order system, transient response of a prototype second order system.	8
IV	Stability of Linear Control Systems: Bounded-input bounded-output stability continuous data systems, zero-input and asymptotic stability of continuous data systems, methods of determining stability, Routh Hurwitz criterion. Root-Locus Technique: Introduction, Properties of the Root Loci, Design aspects of the Root Loci	8
V	Frequency Domain Analysis: $M_r$ (resonant peak) and $\omega_r$ (resonant frequency) and bandwidth of the prototype Second order system, effects of adding a zero to the forward path, effects of adding a pole to the forward path, Nyquist stability criterion, relative stability: gain margin and phase margin, stability analysis with The Bode plot.	8

**Text Book:**

1. B.C. Kuo & Farid Golnaraghi, "Automatic Control Systems", John Wiley India Publication.

**Reference Books:**

1. William A. Wolovich, "Automatic Control Systems", Oxford University Press.
2. Joseph J. Distefano III, Allen R. Stubberud, Ivan J. Williams, "Feedback and Control Systems" Schaums Outlines Series, Tata McGraw Hill Publication.
3. I. J. Nagrath & M. Gopal, "Control System Engineering", New Age International Publishers.

NEC 504 Antenna and Wave Propagation		
Unit	Topic	Lectures
I	<b>Antennas Basics:</b> Introduction, Basic Antenna Parameters, Patterns, Beam Area (or Beam Solid Angle) QA, Radiation Intensity, Beam Efficiency, Directivity D and Gain G, Directivity and Resolution, Antenna Apertures, Effective Height, The radio Communication link, Fields from Oscillating Dipole, Single-to-Noise Ratio(SNR), Antenna Temperature, Antenna Impedance.	8
II	<b>Point Sources and Their Arrays:</b> Introduction, Point Source, Power Theorem and its Application to an Isotropic Source, Radiation Intensity, Arrays of Two Isotropic Point Sources, Non-isotropic but Similar Point Sources and the Principle of Pattern Multiplication, Pattern Synthesis by Pattern Multiplication, Linear Arrays of n Isotropic Point Sources of Equal Amplitude and Spacing, Linear Broadside Arrays with Non- uniform Amplitude Distributions. General Considerations. <b>Electric Dipoles, Thin Liner Antennas and Arrays of Dipoles and Apertures:</b> The Short Electric Dipole, The Fields of a Short Dipole, Radiation Resistance of Short Electric Dipole, Thin Linear Antenna, Radiation Resistance of $\lambda/2$ Antenna, Array of Two Driven $\lambda/2$ Elements: Broadside Case and End-Fire Case, Horizontal Antennas Above a Plane Ground, Vertical Antennas Above a Plane Ground, Yagi-Uda Antenna Design, Long-Wire Antennas, folded Dipole Antennas.	8
III	<b>The Loop Antenna:</b> Design and its Characteristic Properties, Application of Loop Antennas, Far Field Patterns of Circular Loop Antennas with Uniform Current, Slot Antennas, Horn Antennas, Helical Antennas, The Log-Periodic Antenna, Micro strip Antennas. <b>Reflector Antennas:</b> Flat Sheet Reflectors, Corner Reflectors, The Parabola-General Properties, A Comparison Between Parabolic and Corner Reflectors, The Paraboloidal Reflector, Patterns of Large Circular Apertures with Uniform Illumination, Reflector Types (summarized), Feed Methods for Parabolic Reflectors.	8
IV	<b>Ground Wave Propagation:</b> Plane Earth Reflection, Space Wave and Surface Wave. <b>Space Wave Propagation: Introduction,</b> Field Strength Relation, Effects of Imperfect Earth, Effects of Curvature of Earth. <b>Sky wave Propagation:</b> Introduction structural Details of the ionosphere, Wave Propagation Mechanism, Refraction and Reflection of Sky Waves by ionosphere, Ray Path, Critical Frequency, MUF, LUF, OF, Virtual Height and Skip Distance, Relation Between MUF and the Skip Distance, Multi-Hop Propagation, Wave Characteristics.	8

**Text Book:**

1. John D Krauss, Ronald J Marhefka and Ahmad S. Khan, "Antennas and Wave Propagation", Tata McGraw Hill Publication.

**Reference Books:**

1. A. R. Harish, M. Sachidananda, "Antennas and Wave Propagation", Oxford University Press.
2. Edward Conrad Jordan and Keith George Balmain, "Electromagnetic Waves and Radiating Systems", PHI Publication.
3. A. Das, Sisir K. Das, "Microwave Engineering", Tata McGraw Hill Publication.



## LABORATORY

### NEC 551R INTEGRATED CIRCUITS LAB

**Objective:** - To design and implement the circuits to gain knowledge on performance of the circuit and its application. These circuits should also be simulated on Pspice and implemented using TL082, LM741, NE555, ASLK, MPY634 KP connecting wires, Power Supply, function generator and oscilloscope.

1. Design and test a function generator that can generate square wave and triangular wave output for a given frequency and cascade a multiplier MPY634KP in feedback loop to form VCO
2. Voltage to current and current to voltage converters.
3. Second order filters using operational amplifier in universal active filter topology for –
  - a. Low pass filter of specified cutoff frequency
  - b. High pass filter of specified frequency
  - c. Band pass filter with unit gain of specified pass band
  - d. Design a notch filter to eliminate 50Hz power line frequency
4. Wien bridge oscillator using operational amplifier.
5. Astable and monostable multivibrator using IC 555.
6. Design the following amplifiers:
  - a. A unity gain amplifier
  - b. A non-inverting amplifier with a gain of 'A'
  - c. An inverting amplifier with a gain of 'A'
  - d. Log and antilog amplifiers.
  - e. Voltage comparator and zero crossing detectors.
7. Design and test a PLL to get locked to a given frequency 'f'. Measure the locking range of the system and also measure the change in phase of the output signal as input frequency is varied within the lock range.
8. Design and test the integrator for a given time constant.
9. Design and test a high-Q Band pass self-tuned filter for a given center frequency.
10. Design and test an AGC system for a given peak amplitude of sine-wave output.
11. Design and test a Low Dropout regulator using op-amps for a given voltage regulation characteristic and compare the characteristics with TPS7250IC.
12. Design of a switched mode power supply that can provide a regulated output voltage for a given input range using the TPS40200 IC

Note: All listed experiments are compulsory. In addition to it, the Institutes may include more experiments based on the expertise.

### NIC 551: Control System Lab

1. Different Toolboxes in MATLAB, Introduction to Control Systems Toolbox.
2. Determine transpose, inverse values of given matrix.
3. Plot the pole-zero configuration in s-plane for the given transfer function.
4. Determine the transfer function for given closed loop system in block diagram representation.
5. Plot unit step response of given transfer function and find peak overshoot, peak time.
6. Plot unit step response and to find rise time and delay time.
7. Plot locus of given transfer function, locate closed loop poles for different values of k.
8. Plot root locus of given transfer function and to find out  $S_w$ ,  $\zeta$ ,  $\omega_n$  given root & to discuss stability.
9. Plot Bode plot of given transfer function.
10. Plot Bode plot of given transfer function and find gain and phase margins
11. Plot Nyquist plot for given transfer function and to compare the relative stability
12. Plot the Nyquist plot for given transfer function and to discuss closed loop stability, gain

and phase margin.

Note:-In addition to it, Institutes may include more experiments based on the expertise.

### **NEC 552: Communication Lab - 1**

1. To study DSB/ SSB amplitude modulation & determine its modulation factor & power in side bands.
2. To study amplitude demodulation by linear diode detector
3. To study frequency modulation and determine its modulation factor
4. To study PLL 565 as frequency demodulator.
5. To study sampling and reconstruction of Pulse Amplitude modulation system.
6. To study the Sensitivity, Selectivity, and Fidelity characteristics of super heterodyne receiver.
7. To study Pulse Amplitude Modulation
  - a. using switching method
  - b. by sample and hold circuit
8. To demodulate the obtained PAM signal by 2nd order LPF.
9. To study Pulse Width Modulation and Pulse Position Modulation.
10. To plot the radiation pattern of a Dipole, Yagi-Uda and calculate its beam width.
11. To plot the radiation pattern of Horn, Parabolic & helical antenna. Also calculate beam width & element current.
12. Design and implement an FM radio receiver in 88-108 MHz.

### **NEC 553: Microprocessors Lab**

1. Write a program using 8085/ 8086 Microprocessor for Decimal, Hexadecimal addition and subtraction of two Numbers.
2. Write a program using 8085/ 8086 Microprocessor for addition and subtraction of two BCD numbers.
3. To perform multiplication and division of two 8 bit numbers using 8085/ 8086.
4. To find the largest and smallest number in an array of data using 8085/8086 instruction set.
5. To write a program to arrange an array of data in ascending and descending order using 8085/ 8086.
6. To convert given Hexadecimal number into its equivalent ASCII number and vice versa using 8085/ 8086 instruction set.
7. To write a program to initiate 8251 and to check the transmission and reception of character.
8. To interface 8253 programmable interval timer to 8085/ 8086 and verify the operation of 8253 in six different modes.
9. To interface DAC with 8085/ 8086 to demonstrate the generation of square, saw tooth and triangular wave.
10. Serial communication between two 8085/8086 through RS-232 C port.

Note:-In addition, Institutes may include two more experiments based on the expertise.

<b>NEC 601 Microwave Engineering</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	Rectangular Wave Guide: Field Components, TE, TM Modes, Dominant TE <sub>10</sub> mode, Field Distribution, Power, Attenuation. Circular Waveguides: TE, TM modes. Wave Velocities, Microstrip Transmission line (TL), Coupled TL, Strip TL, Coupled Strip Line, Coplanar TL, Microwave Cavities,	<b>8</b>
II	Scattering Matrix, Passive microwave devices: Microwave Hybrid Circuits, Terminations, Attenuators, Phase Shifters, Directional Couplers: Two Hole directional couplers, S Matrix of a Directional coupler, Hybrid Couplers, Microwave Propagation in ferrites, Faraday Rotation, Isolators, Circulators. Spara meter analysis of all components.	<b>8</b>
III	Microwave Tubes: Limitation of Conventional Active Devices at Microwave frequency, Two Cavity Klystron, Reflex Klystron, Magnetron, Traveling Wave Tube, Backward Wave Oscillators: Their Schematic, Principle of Operation, Performance Characteristic and their applications.	<b>8</b>
IV	Solid state amplifiers and oscillators: Microwave Bipolar Transistor, Microwave tunnel diode, Microwave Field-effect Transistor, Transferred electron devices, Avalanche Transit -time devices: IMP ATT Diode, TRAPPAT Diode.	<b>8</b>
V	Microwave Measurements: General setup of a microwave testbench, Slotted line carriage, VSWR Meter, microwave power measurements techniques, Crystal Detector, frequency measurement, wavelength measurements, Impedance and Refection coefficient, VSWR, Insertion and attenuation loss measurements, measurement of antenna characteristics, microwave link design.	<b>8</b>

**Text Books:**

1. Samuel Y. Liao, "Microwave Devices and Circuits", Pearson Education Publication.

**Reference Books:**

1. R.E Collin, "Foundation for Microwave Engineering", John Wiley India Publication
2. A. Das and S.K. Das," Microwave Engineering", Tata McGraw Hill Publication.

<b>NEC 602 Digital Communication</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	Digital Data transmission, Line coding review, Pulse shaping, Scrambling, Digital receivers, Eye diagram, Digital carrier system, Method of generation and detection of coherent & non-coherent binary ASK, FSK & PSK, Differential phase shift keying, quadrature modulation techniques.(QPSK and MSK),M-ary Digital carrier Modulation.	8
II	Concept of Probability, Random variable, Statistical averages, Correlation, Sum of Random Variables, Central Limit Theorem, Random Process, Classification of Random Processes, Power spectral density, Multiple random processes.	8
III	Performance Analysis of Digital communication system: Optimum linear Detector for Binary polar signaling, General Binary Signaling, Coherent Receivers for Digital Carrier Modulations, Signal Space Analysis of Optimum Detection, Vector Decomposition of White Noise Random processes, General Expression for Error Probability of optimum receivers,	8
IV	Spread Spectrum Communications: Frequency Hopping Spread Spectrum (FHSS) systems, Direct Sequence Spread Spectrum, Code Division Multiple Access of DSSS, Multiuser Detection, OFDM Communications	8
V	Measure of Information, Source Encoding, Error Free Communication over a Noisy Channel capacity of a discrete and Continuous Memoryless channel Error Correcting codes: Hamming sphere, Hamming distance and Hamming bound, relation between minimum distance and error detecting and correcting capability, Linear block codes, encoding & syndrome decoding; Cyclic codes, encoder and decoders for systematic cycle codes; convolution codes, code tree & Trellis diagram, Viterbi and sequential decoding, burst error correction, Turbo codes.	8

**Text Book:**

1. B.P.Lathi, "Modern Digital and Analog Communication Systems", Oxford University Press Publication.

**Reference Books:**

1. H. Taub, D.L. Schilling, G. Saha, "Principles of Communications", McGraw-Hill International Publication.
2. Simon Haykin, "Communication Systems", Wiley India Publication.
3. H.P.HSU and D.Mitra, "Analog and Digital Communications", TataMcGraw-Hill Publication.

<b>NEC 603 Integrated Circuit Technology</b>		
Unit	Topic	Lectures
I	Introduction To IC Technology: SSI, MSI, LSI, VLSI Integrated Circuits Crystal Growth and Wafer Preparation: Electronic Grade Silicon, Czochralski Crystal Growth, Silicon Shaping, Processing Considerations. Epitaxy: Vapor -Phase Epitaxy, Molecular Beam Epitaxy, Silicon on Insulators, Epitaxial Evaluation.	8
II	Oxidation: Growth Kinetics, Thin Oxides, Oxidation Techniques and Systems, Oxides Properties. Lithography: Optical Lithography. Photo masks, Wet Chemical Etching. Dielectric and Polysilicon Film Deposition: Deposition Processes, Polysilicon, Silicon Dioxide, Silicon Nitride.	8
III	Diffusion: Diffusion of Impurities in Silicon and Silicon Dioxide, Diffusion Equations, Diffusion Profiles, Diffusion Furnace, Solid, Liquid and Gaseous Sources, Sheet Resistance and its Measurement. Ion-Implantation: Ion-Implantation Technique, Range Theory, Implantation Equipment.	8
IV	Metallization: Metallization Application, Metallization Choices, Physical Vapor Deposition, Vacuum Deposition, Sputtering Apparatus. Packaging of VLSI devices: Package Types, Packaging Design Consideration, VLSI Assembly Technologies, Package Fabrication Technologies.	8
V	VLSI Process Integration: Fundamental Considerations For IC Processing, NMOS IC Technology, CMOS IC Technology, Bipolar IC Technology, Monolithic and Hybrid Integrated Circuits, IC Fabrication	8

**Text Books:**

1. S. M. Sze, "VLSI Technology", McGraw Hill Publication.
2. S.K. Ghandhi, "VLSI Fabrication Principles", Willy-India Pvt. Ltd.

**Reference Books:**

1. J. D. Plummer, M. D. Deal and Peter B. Griffin, "Silicon VLSI Technology: Fundamentals, Practice and Modelling", Pearson Education Publication.
2. Stephen A. Campbell, "Fabrication Engineering at the Micro and Nano scale", Oxford University Press.

## Laboratory

### NEC 651 ANTENNA AND MICROWAVE LAB

1. Study of Reflex Klystron Characteristics.
2. Measurement of guide wavelength and frequency of the signal in a rectangular Waveguide using slotted line carriage in a Micro wave Bench.
3. Measurement of impedance of an unknown load connected at the output end of the slotted line carriage in a Micro wave Bench.
4. Determine the S-parameter of any Three port Tee.
5. Determine the S-parameter of a Magic Tee.
6. Study various parameters of Isolator.
7. Measurement of attenuation of an attenuator and isolation, insertion loss, cross coupling of a circulator.
8. Determine coupling coefficient, Insertion loss, Directivity and Isolation coefficient of anti-Multi-Hole directional coupler.
9. To study working of MIC Components like Micro strip Line, Filter, Directional Coupler, Wilkinson Power Divider, Ring resonator & coupler, antennas & amplifiers.
10. Study of waveguide horn and its radiation pattern and determination of the beam width.
11. Study radiation pattern of any two types of linear antenna.

### NEC 652 COMMUNICATION LAB – II

1. To construct a triangular wave with the help of Fundamental Frequency and its Harmonic component.
2. To construct a Square wave with the help of Fundamental Frequency and its Harmonic component.
3. Study of Pulse code modulation (PCM) and its demodulation using Bread Board.
4. Study of delta modulation and demodulation and observe effect of slope overload.
5. Study of pulse data coding techniques for NRZ formats.
6. Study of Data decoding techniques for NRZ formats.
7. Study of Manchester coding and decoding.
8. Study of Amplitude shift keying modulator and demodulator.
9. Study of Frequency shift keying modulator and demodulator.
10. Study of Phase shift keying modulator and demodulator
11. Study of single bit error detection and correction using Hamming code.
12. Measuring the input impedance and Attenuation of a given Transmission Line

### NEC-653 CAD OF ELECTRONICS LAB

#### PSPICE Experiments

1. (a) Transient Analysis of BJT inverter using step input.  
(b) DC Analysis (VTC) of BJT inverter with and without parameters.
2. (a) Transient Analysis of NMOS inverter using step input.  
(b) Transient Analysis of NMOS inverter using pulse input.  
(c) DC Analysis (VTC) of NMOS inverter with and without parameters.
3. (a) Analysis of CMOS inverter using step input.  
(b) Transient Analysis of CMOS inverter using step input with parameters.  
(c) Transient Analysis of CMOS inverter using pulse input.  
(d) Transient Analysis of CMOS inverter using pulse input with parameters.  
(e) DC Analysis (VTC) of CMOS inverter with and without parameters.
4. Transient & DC Analysis of NOR Gate inverter.

5. Transient & DC Analysis of NAND Gate.
6. VHDL Experiments
  - a. Synthesis and simulation of Full Adder.
  - b. Synthesis and Simulation of Full Subtractor.
  - c. Synthesis and Simulation of 3 X 8 Decoder.
  - d. Synthesis and Simulation of 8 X 1 Multiplexer.
  - e. Synthesis and Simulation of 9 bit odd parity generator.
  - f. Synthesis and Simulation of Flip Flop (D, and T).

### **NEC 655 MICROCONTROLLERS FOR EMBEDDED SYSTEMS LAB**

1. Write a program of Flashing LED connected to port 1 of the 8051 Micro Controller
2. Write a program to generate 10 kHz square wave using 8051.
3. Write a program to show the use of INT0 and INT1 of 8051.
4. Write a program for temperature & to display on intelligent LCD display.
5. Write a program to generate a Ramp waveform using DAC with micro controller.
6. Write a program to Interface GPIO ports in C using MSP430 (blinking LEDs , push buttons)
7. Write a program Interface potentiometer with GPIO.
8. Write a program of PWM based Speed Control of Motor controlled by potentiometer connected to GPIO.
9. Write a program of PWM generation using Timer on MSP430 GPIO.
10. Write a program to Interface an accelerometer.
11. Write a program using USB (Sending data back and forth across a bulk transfer-mode USB connection.)
12. Write a program for Master Slave Communication between 2 MSP430s using SPI
13. Write a program of basic Wi-Fi application – Communication between two MSP430 based sensor nodes.
14. Setting up the CC3100 as a HTTP server.
15. Review of User APIs for TI CC3100 & Initialization and Setting of IP addresses.

## Electives Subjects

<b>NEC 011 Digital Signal Processing</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	<b>Realization of Digital Systems:</b> Introduction, direct form realization of IIR systems, cascade realization of an IIR systems, parallel form realization of an IIR systems, Ladder structures: continued fraction expansion of $H(z)$ , example of continued fraction, realization of a ladder structure, example of a ladder realization.	8
II	<b>Design of Infinite Impulse Response Digital Filters:</b> Introduction to Filters, Impulse Invariant Transformation, Bi-Linear Transformation, All- Pole Analog Filters: Butterworth and Chebyshev, Design of Digital Butterworth and Chebyshev Filters.	8
III	<b>Finite Impulse Response Filter Design:</b> Windowing and the Rectangular Window, Other Commonly Used Windows, Examples of Filter Designs Using Windows, The Kaiser Window.	8
IV	<b>Discrete Fourier Transforms:</b> Definitions, Properties of the DFT, Circular Convolution, Linear Convolution.	8
V	<b>Fast Fourier Transform Algorithms:</b> Introduction, Decimation -In Time (DIT) Algorithm, Computational Efficiency, Decimation in Frequency (DIF) Algorithm.	8

### Text Book:

1. Johnny R. Johnson, "Digital Signal Processing", PHI Publication.

### Reference Books:

1. John G Prokias, Dimitris G Manolakis, "Digital Signal Processing", Pearson Education.
2. Oppenheim & Schafer, "Digital Signal Processing" PHI Publication.
3. Sanjit K. Mitra, "Digital Signal Processing: A Computer-Based Approach", McGraw Hill Publication.
4. Monson Hayes, "Digital Signal Processing", McGraw Hill Education Publication.

<b>NEC 012 Computer Architecture and Organization</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	Introduction to Design Methodology: System Design - System representation, Design Process, the gate level (revision), the register level components and PLD (revision), register level design The Processor Level: Processor level components, Processor level design.	8
II	Processor basics: CPU organization- Fundamentals, Additional features Data Representation - Basic formats, Fixed point numbers, Floating point numbers. Instruction sets - Formats, Types, Programming considerations.	8



III	Data path Design: Fixed point arithmetic - Addition and subtraction, Multiplication and Division, Floating point arithmetic, pipelining.	8
IV	Control Design: basic concepts - introduction, hardwired control, Micro programmed control -introduction, multiplier control unit, CPU control unit, Pipeline control- instruction pipelines, pipeline performance.	8
V	Memory organization: Multi level memories, Address translation, Memory allocation, Caches - Main features, Address mapping, structure vs performance, System Organization: Communication methods- basic concepts, bus control. Introduction to VHDL.	8

**TextBooks:**

1. John P Hayes "Computer Architecture and Organisation", McGraw Hill Publication.

**Reference Books:**

1. M Morris Mano, "Computer System Architecture", Pearson Publication.
2. Carl Hamacher, Zvonko Vranesic and Safwat Zaky, "Computer Organization and Embedded Systems", McGraw Hill Publication.
3. David A. Patterson and John L. Hennessy, "Computer Organization and Design: The Hardware/Software Interface", Elsevier Publication.

<b>NEC 013 Artificial Neural Network</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	<b>Introduction to ANN</b> Features, structure and working of Biological Neural Network Trends in Computing Comparison of BNN and ANN. <b>Basics of Artificial Neural Networks -</b> History of neural network research, characteristics of neural networks terminology, models of neuron McCulloch - Pitts model, Perceptron, Ada line model, Basic learning laws, Topology of neural network architecture.	8
II	<b>Back propagation networks</b> : (BPN) Architecture of feed forward network, single layer ANN, multilayer perceptron, back propagation learning, input - hidden and output layer computation, back propagation algorithm, applications, selection of tuning parameters in BPN, Numbers of hidden nodes, learning.	8
III	<b>Activation &amp; Synaptic Dynamics</b> : Introduction, Activation Dynamics models, synaptic Dynamics models, stability and convergence, recall in neural networks. <b>Basic functional units of ANN for pattern recognition tasks:</b> Basic feed forward, Basic feedback and basic competitive learning neural network. Pattern association, pattern classification and pattern mapping tasks.	8

IV	<p><b>a) Feedforward neural networks -</b>  - Linear responsibility X-OR problem and solution. - Analysis of pattern mapping networks summary of basic gradient search methods.</p> <p><b>b) Feedback neural networks Pattern</b>  Storage networks, stochastic networks and simulated annealing, Boltzmann machine and Boltzmann learning.</p>	8
V	<p><b>Competitive learning neural networks :</b>  Components of CL network pattern clustering and feature. Mapping network, ART networks, Features of ART models, character recognition using ART network.</p> <p><b>Applications of ANN:</b> Pattern classification - Recognition of Olympic games symbols, Recognition of printed Characters. Neocognitron - Recognition of handwritten characters. NET Talk: to convert English text to speech. Recognition of consonant vowel (CV) segments, texture classification and segmentation.</p>	8

**Text Book:**

1. B. Yegnanarayana, "Artificial neural Networks", PHI Publication.

**Reference Books:**

1. S. Raj Sekaran , Vijayalakshmi Pari, " Neural networks, Fuzzy logic and Genetic Algorithms", PHI Publication.
2. Elaine Rich and Kevin Knight, "Artificial Intelligence", TMH Publication.

NEC-013R REAL TIME SYSTEMS		
Unit	Topic	Lectures
I	<p><b>Introduction to Real Time System</b>  Introduction to Real time Embedded System, need for a real-time system, different kinds (reactive, time driven, deadline driven, etc.) Embedded system Design cycle, Types of Real Time systems, Real Time Applications and features, Issues in real time computing, aspects of real-time systems (timeliness, responsiveness, concurrency, predictability, correctness, robustness, fault tolerance and safety, resource limitations, RTOS necessity), real-time requirement specifications, modelling/verifying design tools (UML, state charts, etc.).</p>	8

II	<p><b>Embedded Hardware for Real Time System</b>  Selection criteria for Real time system - Hardware and Software perspective, need for partitioning, criteria for partitioning (performance, criticality, development ease, robustness, fault tolerance and safety, resource limitations, etc.), System Considerations, Basic development environment-host vs target concept, CPU features, Architecture, I/O Ports, on-chip peripherals, Memory, Real time implementation considerations, bus architecture, Introduction to Interrupts, Interrupt vector table, interrupt programming, Pipeline and Parallelism concepts.</p>	10
III	<p><b>Embedded Hardware – On chip Peripherals and Communication protocols</b>  Role of peripherals for Real time systems, On-Chip peripherals &amp; hardware accelerators, Peripherals [Direct Memory Access, Timers, Analog to Digital Conversion (ADC), DAC, Comparator, Pulse Width Modulation (PWM)], Need of real time Communication, Communication Requirements, Timeliness, Dependability, Design Issues, Overview of Real time communication, Real time Communication Peripherals – I2C, SPI &amp; UART.  <b>Introduction to the CCS IDE:</b> its features, project options and basic examples  Analog-to-Digital Converter Lab: Build a data acquisition system  Control Peripherals Lab: Generate and graph a PWM waveform  Direct Memory Access (DMA) Lab: Use DMA to buffer ADC results.</p>	12
IV	<p><b>Embedded Software and RTOS</b>  Software Architecture of real time System, Introduction to RTOS, role of RTOS, foreground Back ground system, pros and cons, Real time kernel, qualities of good RTOS, Functionalities of RTOS – Task Management, I/O management, Memory management, Inter Task Communication, Tasks, Task states, Task control block, attributes of TCB, Context switching, Interrupts handling, Multiprocessing and multitasking.</p>	8
V	<p><b>Introduction to TI C2000:</b> Interface with actuators such as motor control enabling real time capabilities of C2000 Program to demonstrate the Task switching Simulation on CCS IDE To demonstrate the blink led application Using Hwi (Hardware Interrupt: periodically to produce an interrupt using Timers) of TI RTOS.  <b>Programming:</b> demonstrate the Blink led application Using a Swi (Software interrupt) of TI RTOS To introduce two time-based SYS/BIOS services – Clock and Timestamp in TI RTOS; demonstrate the Task synchronization using Semaphores using TI RTOS; demonstrate Inter Task Communication Using of Mailboxes and Queues using TI RTOS; demonstrate the Communication Protocols – I2C, SPI and USART using TI.</p>	10

**Text Book:**

1. Real-Time Systems by Jane W. S. Liu Prentice Hall Publication
2. Krishna .C.M “Real Time Systems” Mc-Graw Hill Publication.

3. Hamid A. Toliyat and Steven G. Campbell, “DSP based Electromechanical Motion Control” CRC Press Publication.
4. Jean J Labrosse, “Embedded System Design blocks”, CMP books Publication
5. John H Davies, “MSP430 Microcontroller Basics” Newnes Publication.

**Reference Book:**

1. TMS320C28x CPU and Instruction Set Reference Guide, TI Literature Publication
2. TMS320x28xx, 28xxx DSP Peripheral Reference Guide, TI Literature Publication
3. C2000 Teaching CD ROM from Texas Instruments Publication
4. Introduction to the TI-RTOS Kernel Workshop Lab Manual, by Texas Instruments Publication.

<b>NEC 014 Advance Semiconductor Devices</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
<b>I</b>	<b>Physics and Properties of Semiconductors:</b> Introduction, Crystal Structure, Energy Bands and Energy Gap, Carrier Concentration at Thermal Equilibrium, Carrier-Transport Phenomena. Phonon, Optical, and Thermal Properties, Heterojunctions and Nanostructures, Basic Equations and Examples. <i>p-n</i> Junctions, Introduction, Depletion Region, Current-Voltage Characteristics, Junction Breakdown, Transient Behavior and Noise, Terminal Functions, Heterojunctions. Metal-Semiconductor Contacts, Metal-Insulator - Semiconductor Capacitors.	<b>8</b>
<b>II</b>	<b>Bipolar Transistors:</b> Static Characteristics, Microwave Characteristics, Related Device Structures, Heterojunction Bipolar Transistor. <b>MOSFETs:</b> Basic Device Characteristics, Nonuniform Doping and Buried-Channel Device, Device Scaling and Short-Channel Effects, MOSFET Structures, Circuit Applications, Nonvolatile Memory Devices, Single-Electron Transistor. JFETs, MESFETs, and MODFETs	<b>8</b>
<b>III</b>	<b>Tunnel Devices:</b> Tunnel Diode, Related Tunnel Devices, Resonant-Tunneling Diode. <b>IMPATT Diodes:</b> Static Characteristics, Dynamic Characteristics, Power and Efficiency, Noise Behavior, Device Design and Performance, BARITT Diode, TUNNETT Diode.	<b>8</b>
<b>IV</b>	Transferred-Electron and Real-Space-Transfer Devices Thyristors and Power Devices <b>Photonic Devices and Sensors:</b> Radioactive Transitions, Light-Emitting Diode (LED), Laser Physics, Laser Operating Characteristics, Specialty Lasers.	<b>8</b>
<b>V</b>	<b>Photodetectors and Solar Cells:</b> Photoconductor, Photodiodes, Avalanche Photodiode, Phototransistor, Charge-Coupled Device (CCD), Metal-Semiconductor-Metal Photodetector, Quantum-Well Infrared Photodetector, Solar Cell. Sensors: Thermal Sensors, Mechanical Sensors, Magnetic Sensors, Chemical Sensors.	<b>8</b>

**Text Book:**

1. S. M. Sze, Kwok K. NG, "Physics of Semiconductor Devices", Wiley Publication.

**Reference Books:**

1. J. P. Colinge and C. A. Colinge, "Physics of Semiconductor Devices", Kluwer Academic Publishers

<b>NEC 021 Industrial Electronics</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
<b>I</b>	<b>Power Semiconductor Devices:</b> Power semiconductor devices their symbols and static characteristics and specifications of switches, types of power electronic circuits Operation, steady state & switch characteristics & switching limits of Power Transistor Operation and steady state characteristics of Power MOSFET and IGBT Thyristor -Operation V- I characteristics, two transistor model, methods of turn-on Operation of GTO, MCT and TRIAC.	<b>8</b>
<b>II</b>	<b>Phase Controlled Rectifiers:</b> Phase Angle Control, Single-phase Half-wave Controlled Rectifier (One quadrant), Single-phase Full-wave Controlled Rectifier (Two quadrant Converters), Performance Factors of Line-commutated Converters, The Performance Measures of Two-pulse Converters, Three phase Controlled Converters <b>Inverters:</b> Introduction Thyristor Inverter Classification, Series Inverters, Parallel Inverter, Three-phase Bridge Inverters, Three-phase Bridge Inverter with Input-circuit Commutation.	<b>8</b>
<b>III</b>	<b>Choppers:</b> Introduction, Principle of Chopper Operation, Control Strategies, step-up/Down Chopper, Jones Chopper. Introduction to basic Cycloconverters. <b>Control of D.C. Drives:</b> Introduction, Basic Machine Equations, Breaking Modes, Schemes for D.C. Motor Speed Control, Single-phase Separately Excited Drives, Braking Operation of Rectifier Controlled Separately excited Motor, Single-phase Separately Excited Drives, Power Factor Improvement, Three-phase Separately Excited Drives, D.C. Chopper Drives	<b>8</b>
<b>IV</b>	<b>Control of A.C. Drives:</b> Introduction, basic Principle of Operation, Squirrel-cage Rotor Design, Speed Control of Induction Motors, stator Voltage Control, Variable Frequency control, Rotor Resistance Control, Slip Power Recovery Scheme, Synchronous Motor Drives	<b>8</b>

**Text Books:**

1. M. H. Rashid, "Power Electronics", Pearson Education Publication.

**Reference Books:**

1. M. D. Singh & K. Khanchandani, "Power Electronics", Tata McGraw Hill Publication.
2. V.R. Moorthy, "Power Electronics: Devices, Circuits and Industrial Applications", Oxford University Press,
3. M.S. Jamil Asghar, "Power Electronics", PHI Publication.

<b>NEC 022R Microcontrollers for Embedded Systems</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	Introduction , Microcontrollers and Embedded systems, Overview of the 8051, Inside the 8051, Addressing modes, assembly programming, 8051 data types and directives, Interfacing with 8051, Programming the 8051 timers.	6
II	MSP430x5x series block diagram, address space, on-chip peripherals (analog and digital), and Register sets. Instruction set, instruction formats, and various addressing modes of 16-bit microcontroller; Sample embedded system on MSP430 microcontroller. Memory Mapped Peripherals, programming System registers, I/O pin multiplexing, pull up/down registers, GPIO control. Interrupts and interrupt programming.	8
III	Watch dog timer, system clocks, Timer & Real Time Clock (RTC), PWM control, timing generation and measurements. Analog interfacing and data acquisition: ADC and Comparator in MSP430, data transfer using DMA.	10
IV	Serial communication basics, Synchronous/Asynchronous interfaces (like UART, USB, SPI, and I2C). UART protocol, I2C protocol, SPI protocol. Implementing and programming UART, I2C, SPI interface using MSP430, Interfacing external devices.	10
V	Internet of Things (IoT) overview and architecture, Overview of wireless sensor networks and design examples. Various wireless connectivity: NFC, ZigBee, Bluetooth, Bluetooth Low Energy, Wi-Fi. Adding Wi-Fi capability to the Microcontroller, Embedded Wi-Fi, User APIs for Wireless and Networking applications, Building IoT applications using CC3100 user API for connecting sensors.	6

**Text Book:**

1. Mazidi Ali Muhammad, Mazidi Gillispie Janice, and Mc Kinlay Rolin D “ The 8051 Microcontroller and Embedded Systems using Assembly and C”, Pearson Publication.
2. John H Davies, “MSP430 Microcontroller Basics” Newnes Publication.

**Reference Book:**

1. TI MSP430x5xx and MSP430x6xx Family User's Guide..

<b>NEC 023 Analog Signal Processing</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	Introduction to domains and the analogue/digital trade off, Introduction to basic building blocks: null or, voltage feedback amplifier, operation transconductance amplifier, current conveyer, current feedback amplifier. Analog signal filtering: introduction to bilinear transfer functions and active realizations. First-order and second-order filter realization, filter design parameters (Q and $\omega_0$ ), frequency response, effect of finite gain of op-amp, realization of Single-Amplifier Biquad and General Impedance Convertor circuit.	8
II	Ideal low-pass filter, Butterworth and Chebyshev magnitude response, pole locations, low-pass filter specifications.	8
III	Delay equalization: equalization procedures, equalization with first-order and second-order modules, strategies for equalization design. Definition of Bode sensitivity.	8
IV	Properties of Lossless ladders, the general impedance convertor (GIC), optimal design of the GIC, realization of simple ladders, Gorski-Popiel's Embedding Technique, Bruton's FDNR technique, creating negative components.	8

**Text Books:**

1. R. Schaumann and M.E. Valkenberg, " Design of Analog Circuits", Oxford University

<b>NEC 024R Advanced Digital System Design using Verilog</b>		
<b>Unit</b>	<b>Topic</b>	<b>Lectures</b>
I	<b>Introduction to Verilog HDL:</b> Verilog as HDL, Levels of Design Description, Concurrency, Simulation and Synthesis, Functional Verification, System Tasks, Programming Language Interface (PLI), Module, Simulation and Synthesis Tools.  <b>Language Constructs and Conventions:</b> Introduction, Keywords, Identifiers, White Space Characters, Comments, Numbers, Strings, Logic Values, Strengths, Data Types, Scalars and Vectors, Parameters, Operators.	8
II	<b>Gate Level Modeling:</b> Introduction, AND Gate Primitive, Module Structure, Other Gate Primitives, Illustrative Examples, Tri-State Gates, Array of Instances of Primitives, Design of Flip-flops with Gate Primitives, Delays, Strengths and Construction Resolution, Net Types, Design of Basic Circuits.  <b>Modeling at Dataflow Level:</b> Introduction, Continuous Assignment Structure, Delays and Continuous Assignments, Assignment to Vectors, Operators.	8

III	<p><b>Behavioral Modeling:</b> Introduction, Operations and Assignments, Functional Bifurcation, Initial Construct, Always Construct, Assignments with Delays, Wait construct, Multiple Always Blocks, Designs at Behavioral Level, Blocking and Non-Blocking Assignments, The case statement, Simulation Flow <i>if</i> and <i>if-else</i> constructs, Assign-De-Assign construct, Repeat construct, for loop, the Disable construct, While loop, Forever loop, Parallel Blocks, Force-Release construct, Event.</p> <p><b>Switch Level Modeling:</b> Basic Transistor Switches, CMOS Switch, Bi-directional Gates, Time Delays with Switch Primitives, Instantiations with Strengths and Delays, Strength Contention with Trireg Nets, Exercises.</p>	8
IV	<p><b>System Tasks, Functions and Compiler Directives:</b> Parameters, Path Delays, Module Parameters, System Tasks and Functions, File-Based Tasks and Functions, Computer Directives, Hierarchical Access, User Defined Primitives.</p> <p><b>Sequential Circuit Description:</b> Sequential Models – Feedback Model, Capacitive Model, Implicit Model, Basic Memory Components, Functional Register, Static Machine Coding, Sequential Synthesis.</p>	8
V	<p><b>Components Test and Verification:</b> Test Bench- Combinational Circuit Testing, Sequential Circuit Testing, Test Bench Techniques, Design Verification, Assertion Verification.</p> <p>Factoring, Decomposition, BDD, Ordered BDD, LPDD, Fault Detection and Analysis in combinational and sequential systems.</p>	8

**Text Books:**

1. T.R. Padmanabhan, B. Bala Tripura Sundari , Design through Verilog HDL”, Wiley Publication.
2. Zainalabdien Navabi, Verilog Digital System Design, TMH Publication.
3. Richard F. Tinder, “Engineering Digital Design”, Academic Press Publication.

**Reference Books:**

1. Stephen. Brown and Zvonko Vranesic,” Fundamentals of Logic Design with Verilog Design”, TMH Publication.
2. Sunggu Lee ,”Advanced Digital Logic Design using Verilog, State Machine & Synthesis for FPGA “, Cengage Learning Publication.
3. Samir Palnitkar , “Verilog HDL”, 2nd Edition, Pearson Education Publication.
4. Michael D. Ciletti , “Advanced Digital Design with Verilog HDL “, PHI Publication.
5. Parag K. Lala, “Digital System Design Using PLDs”, PHI India Ltd.