

DR. A.P.J. ABDUL KALAM TECHNICAL
UNIVERSITY, UTTAR PRADESH, LUCKNOW



EVALUATION SCHEME & SYLLABUS

FOR

B. TECH. THIRD YEAR
FIFTH SEMESTER

ELECTRONICS AND COMPUTER ENGINEERING

AS PER

AICTE MODEL CURRICULUM

[Effective from the Session: 2021-22]

ELECTRONICS AND COMPUTER ENGINEERING

B.Tech. V Semester Electronics and Computer Engineering

S. No.	Course Code	Course Title	Periods			Evaluation Scheme				End Semester		Total	Credits
			L	T	P	CT	TA	Total	PS	TE	PE		
1	KECZ-501	Integrated Circuit Design	3	1	0	30	20	50		100		150	4
2	KECZ-502	Digital Signal Processing	3	1	0	30	20	50		100		150	4
3	KECZ-502	Database Management System	3	1	0	30	20	50		100		150	4
4	KECZ-051-054	Department Elective-I	3	0	0	30	20	50		100		150	3
5	KECZ-055-058	Department Elective-II	3	0	0	30	20	50		100		150	3
6	KECZ-551	Integrated Circuits Lab	0	0	2				25		25	50	1
7	KECZ-552	Database Management System Lab	0	0	2				25		25	50	1
8	KECZ-553	Digital Signal Processing Lab	0	0	2				25		25	50	1
9	KECZ-554	Mini Project/Internship **	0	0	2				50			50	1
10	KNC501	Constitution of India, Law and Engineering	2	0	0	15	10	25		50			NC
11		MOOCs (Essential for Hons. Degree)											
		Total										950	22

**The Mini Project or Internship (4weeks) conducted during summer break after IV Semester and will be assessed during Vth Semester.

Course Code

Course Title

Department Elective-I

KECZ-051	Computer Architecture and Organization
KECZ-052	Industrial Electronics
KECZ-053	Optical Communication System
KECZ-054	Advance Digital Design using Verilog

Department Elective-II

KECZ-055	Data Analytics
KECZ-056	Web Designing
KECZ-057	Computer Graphics
KECZ-058	Object Oriented System Design

**B.Tech 3rd Year
V Semester
Syllabus**

ELECTRONICS AND COMPUTER ENGINEERING

KECZ-501	INTEGRATED CIRCUITS	3L:1T:0P	4 Credits
Unit	Topics	Lectures	
I	The 741 IC Op-Amp: General operational amplifier stages (bias circuit, the input stage, the second stage, the output stage, short circuit protection circuitry), device parameters, DC and AC analysis of input stage, second stage and output stage, gain, frequency response of 741, a simplified model, slew rate, relationship between ft and slew rate.	8	
II	Linear Applications of IC Op-Amps: Op-Amp based V-I and I-V converters, instrumentation amplifier, generalized impedance converter, simulation of inductors. Active Analog filters: Sallen Key second order filter, Designing of second order low pass and high pass Butterworth filter, Introduction to band pass and band stop filter, all pass active filters, KHN Filters. Introduction to design of higher order filters.	8	
III	Frequency Compensation & Nonlinearity: Frequency Compensation, Compensation of two stage Op-Amps, Slewing in two stage Op-Amp. Nonlinearity of Differential Circuits, Effect of Negative feedback on Nonlinearity.	4	
	Non-Linear Applications of IC Op-Amps: Basic Log–Anti Log amplifiers using diode and BJT, temperature compensated Log–Anti Log amplifiers using diode, peak detectors, sample and hold circuits. Op-amp as a comparator and zero crossing detector, astable multivibrator & monostable multivibrator. Generation of triangular waveforms, analog multipliers and their applications.	8	
IV	Digital Integrated Circuit Design: An overview, CMOS logic gate circuits basic structure, CMOS realization of inverters, AND, OR, NAND and NOR gates. Latches and Flip flops: the latch, CMOS implementation of SR flip-flops, a simpler CMOS implementation of the clocked SR flip-flop, CMOS implementation of J-K flip-flops, D flip- flop circuits.	6	
V	Integrated Circuit Timer: Timer IC 555 pin and functional block diagram, Monostable and Astable multivibrator using the 555 IC. Voltage Controlled Oscillator: VCO IC 566 pin and functional block diagram and applications. Phase Locked Loop (PLL): Basic principle of PLL, block diagram, working, Ex-OR gates and multipliers as phase detectors, applications of PLL.	6	

Text Book:

1. Microelectronic Circuits, Sedra and Smith, 7th Edition, Oxford, 2017.
2. Behzad Razavi: Design of Analog CMOS Integrated Circuits, TMH

Reference Books:

1. Gayakwad: Op-Amps and Linear Integrated Circuits, 4th Edition Prentice Hall of India, 2002.
2. Franco, Analog Circuit Design: Discrete & Integrated, TMH, 1st Edition.
3. Salivahnan, Electronics Devices and Circuits, TMH, 3rd Edition, 2015
4. Millman and Halkias: Integrated Electronics, TMH, 2nd Edition, 2010

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Explain complete internal analysis of Op-Amp 741-IC.
2. Examine and design Op-Amp based circuits and basic components of ICs such as various types of filter.
3. Implement the concept of Op-Amp to design Op-Amp based non-linear applications and wave-shaping circuits.
4. Analyse and design basic digital IC circuits using CMOS technology.
5. Describe the functioning of application specific ICs such as 555 timer ,VCO IC 566 and PLL.

ELECTRONICS AND COMPUTER ENGINEERING

KECZ-502	DIGITAL SIGNAL PROCESSING	3L:1T:0P	4 Credits
Unit	Topics		Lectures
I	Introduction to Digital Signal Processing: Basic elements of digital signal processing, advantages and disadvantages of digital signal processing, Technology used for DSP. Realization of Digital Systems: Introduction- basic building blocks to represent a digital system, recursive and non-recursive systems, basic structures of a digital system: Canonic and Non-Canonic structures. IIR Filter Realization: Direct form, cascade realization, parallel form realization, Ladder structures- continued fraction expansion of $H(z)$, example of continued fraction, realization of a ladder structure, design examples. FIR Filter Realization: Direct, Cascade, FIR Linear Phase Realization and design examples.		8
II	Infinite Impulse Response Digital (IIR) Filter Design: Introduction to Filters, Impulse Invariant Transformation, Bi-Linear Transformation, All- Pole Analog Filters: Butterworth and Chebyshev, Design of Digital Butterworth and Chebyshev Filters, Frequency Transformations.		8
III	Finite Impulse Response Filter (FIR) Design: Windowing and the Rectangular Window, Gibb's phenomenon, Other Commonly Used Windows (Hamming, Hanning, Bartlett, Blackmann, Kaiser), Examples of Filter Designs Using Windows. Finite Word length effects in digital filters: Coefficient quantization error, Quantization noise – truncation and rounding, Limit cycle oscillations-dead band effects.		8
IV	DFT & FFT: Definitions, Properties of the DFT, Circular Convolution, Linear Convolution using Circular Convolution, Decimation in Time (DIT) Algorithm, Decimation in Frequency (DIF) Algorithm.		8
V	Multirate Digital Signal Processing (MDSP): Introduction, Decimation, Interpolation, Sampling rate conversion: Single and Multistage, applications of MDSP- Subband Coding of Speech signals, Quadrature mirror filters, Advantages of MDSP.		8

Text Books:

1. John G Prokias, Dimitris G Manolakis, Digital Signal Processing. Pearson , 4th Edition, 2007
2. Johnny R. Johnson, Digital Signal Processing, PHI Learning Pvt Ltd., 2009.
3. S. Salivahanan, A. Vallavaraj, Digital Signal Processing, TMH, 4th Edition 2017.
4. Oppenheim & Schaffer, Digital Signal Processing. Pearson Education 2015
5. S.K. Mitra, 'Digital Signal Processing–A Computer Based Approach, TMH, 4th Edition.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Design and describe different types of realizations of digital systems (IIR and FIR) and their utilities.
2. Select design parameters of analog IIR digital filters (Butterworth and Chebyshev filters) and implement various methods such as impulse invariant transformation and bilinear transformation of conversion of analog to digital filters.
3. Design FIR filter using various types of window functions.
4. Define the principle of discrete Fourier transform & its various properties and concept of circular and linear convolution. Also, students will be able to define and implement FFT i.e. a fast computation method of DFT.
5. Define the concept of decimation and interpolation. Also, they will be able to implement it in various practical applications.

ELECTRONICS AND COMPUTER ENGINEERING

Database Management System (KECZ-503)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to:		
CO 1	Apply knowledge of database for real life applications.	K ₃
CO 2	Apply query processing techniques to automate the real time problems of databases.	K ₃ , K ₄
CO 3	Identify and solve the redundancy problem in database tables using normalization.	K ₂ , K ₃
CO 4	Understand the concepts of transactions, their processing so they will familiar with broad range of database management issues including data integrity, security and recovery.	K ₂ , K ₄
CO 5	Design, develop and implement a small database project using database tools.	K ₃ , K ₆
DETAILED SYLLABUS		3-1-0
Unit	Topic	Proposed Lecture
I	Introduction: Overview, Database System vs File System, Database System Concept and Architecture, Data Model Schema and Instances, Data Independence and Database Language and Interfaces, Data Definitions Language, DML, Overall Database Structure. Data Modeling Using the Entity Relationship Model: ER Model Concepts, Notation for ER Diagram, Mapping Constraints, Keys, Concepts of Super Key, Candidate Key, Primary Key, Generalization, Aggregation, Reduction of an ER Diagrams to Tables, Extended ER Model, Relationship of Higher Degree.	08
II	Relational data Model and Language: Relational Data Model Concepts, Integrity Constraints, Entity Integrity, Referential Integrity, Keys Constraints, Domain Constraints, Relational Algebra, Relational Calculus, Tuple and Domain Calculus. Introduction on SQL: Characteristics of SQL, Advantage of SQL. SQL Data Type and Literals. Types of SQL Commands. SQL Operators and Their Procedure. Tables, Views and Indexes. Queries and Sub Queries. Aggregate Functions. Insert, Update and Delete Operations, Joins, Unions, Intersection, Minus, Cursors, Triggers, Procedures in SQL/PL SQL	08
III	Data Base Design & Normalization: Functional dependencies, normal forms, first, second, 8 third normal forms, BCNF, inclusion dependence, loss less join decompositions, normalization using FD, MVD, and JDs, alternative approaches to database design	08
IV	Transaction Processing Concept: Transaction System, Testing of Serializability, Serializability of Schedules, Conflict & View Serializable Schedule, Recoverability, Recovery from Transaction Failures, Log Based Recovery, Checkpoints, Deadlock Handling. Distributed Database: Distributed Data Storage, Concurrency Control, Directory System.	08
V	Concurrency Control Techniques: Concurrency Control, Locking Techniques for Concurrency Control, Time Stamping Protocols for Concurrency Control, Validation Based Protocol, Multiple Granularity, Multi Version Schemes, Recovery with Concurrent Transaction, Case Study of Oracle.	08
Text books:		
<ol style="list-style-type: none"> 1. Korth, Silbertz, Sudarshan, " Database Concepts", McGraw Hill 2. Date C J, "An Introduction to Database Systems", Addison Wesley 3. Elmasri, Navathe, " Fundamentals of Database Systems", Addison Wesley 4. O'Neil, Databases, Elsevier Pub. 5. RAMAKRISHNAN "Database Management Systems", McGraw Hill 6. Leon & Leon, "Database Management Systems", Vikas Publishing House 7. Bipin C. Desai, " An Introduction to Database Systems", Gagotia Publications 8. Majumdar & Bhattacharya, "Database Management System", TMH 		

KECZ-051	Computer Architecture and Organization	3L:0T:0P	3 Credits	
Unit	Topics			Lectures
I	Introduction to Design Methodology: System Design – System representation, Design Process, the gate level (revision), the register level components and PLD (revision), register level design The Processor Level: Processor level components, Processor level design.			8
II	Processor basics: CPU organization- Fundamentals, Additional features Data Representation - Basic formats, Fixed point numbers, Floating point numbers. Instruction sets - Formats, Types, Programming considerations.			8
III	Data path Design: Fixed point arithmetic - Addition and subtraction, Multiplication and Division, Floating point arithmetic, pipelining.			8
IV	Control Design: basic concepts - introduction, hardwired control, Micro programmed control -introduction, multiplier control unit, CPU control unit, Pipeline control- instruction pipelines, pipeline performance.			8
V	Memory organization: Multi level memories, Address translation, Memory allocation, Caches - Main features, Address mapping, structure vs performance, System Organization: Communication methods- basic concepts, bus control. Introduction to VHDL.			8

Text Book:

1. John P Hayes "Computer Architecture and Organization", 3rd Edition McGraw Hill Publication. (2017)
2. M Morris Mano, "Computer System Architecture", 3rd Edition, Pearson (2017)

Reference Books:

1. Carl Hamacher, Zvonko Vranesic and Safwat Zaky, "Computer Organization and Embedded Systems", McGraw Hill Publication. (2009)
2. David A. Patterson and John L. Hennessy, "Computer Organization and Design: The Hardware/Software Interface", Elsevier Publication. (2007)

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Discuss about the basic concepts of system design methodology and processor level design.
2. Explain the basics of processor and basic formats of data representation.
3. Perform fixed and floating point arithmetic operations.
4. Describe the basic concepts of control design and pipeline performance.
5. Explain the architecture and functionality of central processing unit.

KECZ-052		INDUSTRIAL ELECTRONICS	3L:0T:0P	3 Credits
Unit	Topics			Lectur es
I	Introduction to Power Switching Devices: Description of working & constructional features, Switching Characteristics, ratings and Applications of Power Transistor, Power MOSFET, SCR, DIAC, TRIAC, IGBT and MCT.			8
II	SCR Performance and Applications: Protection of SCR, SCR Triggering and Commutation Circuits/Methods, Series and Parallel operation of SCR, two transistor model of SCR, , Describe Construction & Working of Opto- Isolators, Opto-TRIAC, Opto-SCR.			8
III	Power Converter Performance & Applications: Introduction to Basic Power Converters Architecture - Single Phase, there performance under different types of Loads, Average/RMS output Voltage & Current, Freewheeling Diode, Feedback Diode, State Relay using Opto SCR, SMPS and UPS functioning through Block Diagrams.			8
IV	Timers & Delay Elements, High Frequency Power Heating, Sensor and Actuators: RC Base Constant Timers, Timer Circuits using SCR, IC-555, Programmable Timer and their Industrial Applications, Induction Heating and Dielectric Heating System and Their Applications, Sensors, Transducers, and Transmitters for Measurement, Control & Monitoring : Thermoresistive Transducer, Photoconductive Transducers, Pressure Transducers, Flow Transducers, Level Sensors, Speed Sensing, Vibration Transducers, Variable-Frequency Drives, Stepper Motors and Servomotor Drives.			8
V	Automation and Control: Data Communications for Industrial Electronics, Telemetry, SCADA & Automation, AC & DC Drives, Voltage & Power Factor Control through Solid State Devices, Soft Switching, Industrial Robots.			8

Text Books:

1. M.H. Rashid, "Power Electronics: Circuits, Devices & Applications", Pearson, 4rd Edition, 2013.
2. P.C.Sen, "Power Electronics", McGraw Hill Education (India) Pvt. Ltd 2nd Ed, 2017
3. V.R. Moorthy, "Power Electronics: Devices, Circuits and Industrial Applications" Oxford University Press, 2007.
4. B. Paul, Industrial Electronic and Control, Prentice Hall of India Private Limited (2004).
5. Ned Mohan, T.M. Undeland and W.P. Robbins, "Power Electronics: Converters, Applications and Design", Wiley India Ltd, 2008.
6. P.S. Bhimbra, "Power Electronics", Khanna Publishers.

Reference Books:

1. Thomas E. Kissell, Industrial Electronics: Applications for Programmable Controllers, Instrumentation and Process Control, and Electrical Machines and Motor Controls, 3rd edition, 2003, Prentice Hall.
2. Chakrabarti & Rai, "Fundamentals of Power Electronics & Drives" Dhanpat Rai & Sons.
3. S.N.Singh, "A Text Book of Power Electronics" Dhanpat Rai & Sons.
4. G.K. Dubey, Power Semiconductor Controlled Drives, Prentice Hall inc. (1989).

Course Outcomes: At the end of this course students will be able to:

1. Describe the characteristics, operation of power switching devices and identify their ratings and applications.
2. Recognize the requirement of SCR Protection and describe the Functioning of SCR.
3. Analyze and design Power Converter based on SCR for various Industrial Applications.
4. Explain High Frequency Heating Systems, Timers, Relevant Sensors & Actuator and their application in industrial setting.
5. Explain and apply Data Communication, Telemetry & SCADA System in industrial applications.

KECZ 054	ADVANCED DIGITAL DESIGN USING VERILOG	3L:0T:0P	3 Credits
Unit	Topic	Lectures	
I	Introduction to Mixed Logic, Logic Representation and Minimization with cost, Multiple output minimization, Entered Variable K- Map including don't care handling, XOR Pattern Handling.	8	
II	Combinational Circuit Design, Multiplexers, Decoders, Encoders, Code Comparators, Adders, Subtractors, Multipliers, Introduction to Verilog, Behavioral and Structural specification of logic circuits, Boolean function implementation using Verilog, Timing Analysis, Hazard Detection and Elimination	8	
III	Synchronous Sequential Circuits Design, Mapping Algorithm, Synchronous State Machines, ASM Charts, Asynchronous Sequential Circuit Design, Races, Multi-level minimization and optimization.	8	
IV	Factoring, Decomposition, BDD, Ordered BDD, LPDD, Fault Detection and Analysis in combinational and sequential systems, Path Sensitization method, Boolean Difference Method, Initial State Method.	8	
V	Study of programmable logic families, PLD, CPLD, FPGA, ASIC, PLA, Architectures, Design of Combinational and sequential circuits using CPLD and FPGA, Design Examples.	8	

Text Books:

1. Richard F. Tinker, "Engineering Digital Design", Academic Press.
2. Parag K. Lala, "Digital system Design Using PLDs", PHI India Ltd.
3. Stephen Brown and Zvonko Vranesiv, "Fundamental of Digital Logic with Verilog Design", McGraw Hill.

Reference Books:

1. John Williams, "Digital VLSI Design with Verilog", Springer Publication..
2. Samuel C. Lee, "Digital Circuit and Logic Design", PHI India Ltd.
3. Alexander Miczo, "Digital Logic Testing and Simulation", Wiley Interscience.

COURSE OUTCOME: After completion of the course student will be able to

1. Describe mixed logic circuits and their implementation.
2. Implement combinational circuits using mixed logic and Verilog.
3. Design sequential circuits using mixed logic and Verilog with mapping of Algorithm.
4. Understand faults and its elimination in sequential and combinational circuits.
5. Understand the working of programmable logic families.

KECZ-054	OPTICAL COMMUNICATION SYSTEM	3L:0T:0P	3 Credits	
Unit	Topics			Lectures
I	Introduction to Optical Communication: Optical Spectral Band with Operating Windows, General Communication System, Optical Communication System with its advantages. Optical Fiber Waveguides: Ray Theory of Transmission with TIR, Acceptance Angle, Numerical Aperture and Skew Rays, Electromagnetic Mode Theory for Optical Propagation, Modes in a Planar Guide, Phase and Group Velocity, Phase Shift with Total Internal Reflection, Evanescent Field, Goos-Haenchen Shift, Cylindrical Fiber Modes, Mode Coupling, Step Index fibers Vs Graded Index fibers, Single Mode Fibers- Cut off wavelength, MFD & Spot Size.			08
II	Signal Loss in Optical Fibers: Attenuation, Material Absorption Losses (Intrinsic and Extrinsic absorption), types of Linear and Non-Linear Scattering Losses, Fiber Bending Losses, Kerr Effect. Dispersion: Introduction with its types: Chromatic / Intramodal Dispersion (Material and Waveguide Dispersion), Intermodal dispersion (for MSI and MGI fibers), Overall (Total) Fiber Dispersion in Multimode and Single Mode Fiber, Dispersion Modified Single Mode Fibers, Polarization & Fiber Birefringence.			08
III	Optical Sources: LEDs- Introduction to LEDs & Materials used for fabrication, LED Power and Efficiency, LED Structures, LED Characteristics, Modulation Bandwidth. Laser Diodes- Introduction, Optical Feedback & Laser Oscillations, Resonant Frequencies, Laser Modes, and Threshold Condition for Laser Oscillation, Laser Diode Rate Equations, Semiconductor injection Laser- Efficiency, Laser Single Mode operation, Reliability of LED & ILD.			08
IV	Power Launching in Fiber: Source to Fiber Power Launching and Coupling Techniques, Power Launching Vs Wavelength, Equilibrium Numerical Aperture. Photo Detectors: Introduction, Physical Principles of Photodiodes: The PIN Photo Detector, Avalanche Photodiodes, Temperature Effect on Avalanche Gain, Detector Response Time, Photo Detector Noise: Noise Sources, Signal to Noise Ratio, Comparison of Photo Detectors, Fundamental Receiver Operation with Digital Signal Transmission.			08
V	Digital Receiver Performance: Probability of Error / BER, Receiver Sensitivity & The Quantum Limit, Error Control Techniques, Eye Diagram Pattern Features, Coherent Detection: Homodyne Detection and Heterodyne Detection, Digital links: Point to Point Links, Power Penalties, Multichannel & Multiplexing Transmission Techniques, basic concept of Free Space Optics (FSO) based Communication System.			08

Text Book:

1. John M. Senior, "Optical Fiber Communications", Pearson, 3rd Edition, 2010.
2. Gerd Keiser, "Optical Fiber Communications", McGraw Hill, 5th Edition, 2013.
3. Govind P. Agrawal, "Fiber Optic Communication Systems", John Wiley, 3rd Edition, 2004.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Define and explain the basic concepts and theory of optical communication.
2. Describe the signal losses with their computation and dispersion mechanism occurring inside the optical fiber cable.
3. Differentiate the optical sources used in optical communication with their comparative study.
4. Identify different optical components on receiver side; assemble them to solve real world problems related to optical communication systems.
5. Evaluate the performance of an optical receiver to get idea about power budget and ultimately be an engineer with adequate knowledge in optical domain.

Data Analytics (KECZ-055)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to :		
CO 1	Describe the life cycle phases of Data Analytics through discovery, planning and building.	K1,K2
CO 2	Understand and apply Data Analysis Techniques.	K2, K3
CO 3	Implement various Data streams.	K3
CO 4	Understand item sets, Clustering, frame works & Visualizations.	K2
CO 5	Apply R tool for developing and evaluating real time applications.	K3,K5,K6
DETAILED SYLLABUS		3-0-0
Unit	Topic	Proposed Lecture
I	Introduction to Data Analytics: Sources and nature of data, classification of data (structured, semi-structured, unstructured), characteristics of data, introduction to Big Data platform, need of data analytics, evolution of analytic scalability, analytic process and tools, analysis vs reporting, modern data analytic tools, applications of data analytics. Data Analytics Lifecycle: Need, key roles for successful analytic projects, various phases of data analytics lifecycle – discovery, data preparation, model planning, model building, communicating results, operationalization.	08
II	Data Analysis: Regression modeling, multivariate analysis, Bayesian modeling, inference and Bayesian networks, support vector and kernel methods, analysis of time series: linear systems analysis & nonlinear dynamics, rule induction, neural networks: learning and generalisation, competitive learning, principal component analysis and neural networks, fuzzy logic: extracting fuzzy models from data, fuzzy decision trees, stochastic search methods.	08
III	Mining Data Streams: Introduction to streams concepts, stream data model and architecture, stream computing, sampling data in a stream, filtering streams, counting distinct elements in a stream, estimating moments, counting oneness in a window, decaying window, Real-time Analytics Platform (RTAP) applications, Case studies – real time sentiment analysis, stock market predictions.	08
IV	Frequent Itemsets and Clustering: Mining frequent itemsets, market based modelling, Apriori algorithm, handling large data sets in main memory, limited pass algorithm, counting frequent itemsets in a stream, clustering techniques: hierarchical, K-means, clustering high dimensional data, CLIQUE and ProCLUS, frequent pattern based clustering methods, clustering in non-euclidean space, clustering for streams and parallelism.	08
V	Frame Works and Visualization: MapReduce, Hadoop, Pig, Hive, HBase, MapR, Sharding, NoSQL Databases, S3, Hadoop Distributed File Systems, Visualization: visual data analysis techniques, interaction techniques, systems and applications. Introduction to R - R graphical user interfaces, data import and export, attribute and data types, descriptive statistics, exploratory data analysis, visualization before analysis, analytics for unstructured data.	08

Text books and References:

1. Michael Berthold, David J. Hand, Intelligent Data Analysis, Springer
2. Anand Rajaraman and Jeffrey David Ullman, Mining of Massive Datasets, Cambridge University Press.
3. Bill Franks, Taming the Big Data Tidal wave: Finding Opportunities in Huge Data Streams with Advanced Analytics, John Wiley & Sons.
4. John Garrett, Data Analytics for IT Networks : Developing Innovative Use Cases, Pearson Education
5. Michael Minelli, Michelle Chambers, and Ambiga Dhiraj, "Big Data, Big Analytics: Emerging Business Intelligence and Analytic Trends for Today's Businesses", Wiley
6. David Dietrich, Barry Heller, Beibei Yang, "Data Science and Big Data Analytics", EMC Education Series, John Wiley
7. Frank J Ohlhorst, "Big Data Analytics: Turning Big Data into Big Money", Wiley and SAS Business Series
8. Colleen Mccue, "Data Mining and Predictive Analysis: Intelligence Gathering and Crime Analysis", Elsevier
9. Michael Berthold, David J. Hand," Intelligent Data Analysis", Springer
10. Paul Zikopoulos, Chris Eaton, Paul Zikopoulos, "Understanding Big Data: Analytics for Enterprise Class Hadoop and Streaming Data", McGraw Hill
11. Trevor Hastie, Robert Tibshirani, Jerome Friedman, "The Elements of Statistical Learning", Springer
12. Mark Gardner, "Beginning R: The Statistical Programming Language", Wrox Publication
13. Pete Warden, Big Data Glossary, O'Reilly
14. Glenn J. Myatt, Making Sense of Data, John Wiley & Sons

ELECTRONICS AND COMPUTER ENGINEERING

Web Designing (KECZ-056)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to:		
CO 1	Understand principle of Web page design and about types of websites	K ₃ , K ₄
CO 2	Visualize and Recognize the basic concept of HTML and application in web designing.	K ₁ , K ₂
CO 3	Recognize and apply the elements of Creating Style Sheet (CSS).	K ₂ , K ₄
CO 4	Understand the basic concept of Java Script and its application.	K ₂ , K ₃
CO 5	Introduce basics concept of Web Hosting and apply the concept of SEO	K ₂ , K ₃
DETAILED SYLLABUS		3-0-0
Unit	Topic	Proposed Lecture
I	Introduction : Basic principles involved in developing a web site, Planning process , Domains and Hosting, Responsive Web Designing , Types of Websites (Static and Dynamic Websites), Web Standards and W3C recommendations, Introduction to HTML: What is HTML , HTML Documents, Basic structure of an HTML document , Creating an HTML document , Mark up Tags , Heading-Paragraphs , Line Breaks	08
II	Elements of HTML: HTML Tags., Working with Text , Working with Lists, Tables and Frames, Working with Hyperlinks, Images and Multimedia, Working with Forms and controls	08
III	Concept of CSS: Creating Style Sheet, CSS Properties , CSS Styling(Background, Text Format, Controlling Fonts) , Working with block elements and objects , Working with Lists and Tables , CSS Id and Class, Box Model(Introduction, Border properties, Padding Properties, Margin properties) CSS Advanced(Grouping, Dimension, Display, Positioning, Floating, Align, Pseudo class, Navigation Bar, Image Sprites, Attribute sector) , CSS Color , Creating page Layout and Site Designs.	08
IV	Introduction to Client Side Scripting , Introduction to Java Script , Javascript Types , Variables in JS, Operators in JS , Conditions Statements , Java Script Loops, JS Popup Boxes , JS Events , JS Arrays, Working with Arrays, JS Objects ,JS Functions , Using Java Script in Real time , Validation of Forms, Related Examples	08
V	Web Hosting: Web Hosting Basics , Types of Hosting Packages, Registering domains , Defining Name Servers , Using Control Panel, Creating Emails in Cpanel , Using FTP Client, Maintaining a Website Concepts of SEO : Basics of SEO, Importance of SEO, Onpage Optimization Basics	08
Text Books:		
<ol style="list-style-type: none"> 1. Steven M. Schafer, “HTML, XHTML, and CSS Bible, 5ed”, Wiley India 2. Ian Pouncey, Richard York, “Beginning CSS: Cascading Style Sheets for Web Design”, Wiley India 		

ELECTRONICS AND COMPUTER ENGINEERING

Computer Graphics (KECZ-057)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to:		
CO 1	Understand the graphics hardware used in field of computer graphics.	K ₂
CO 2	Understand the concept of graphics primitives such as lines and circle based on different algorithms.	K ₂ , K ₄
CO 3	Apply the 2D graphics transformations, composite transformation and Clipping concepts.	K ₄
CO 4	Apply the concepts of and techniques used in 3D computer graphics, including viewing transformations.	K ₂ , K ₃
CO 5	Perform the concept of projections, curve and hidden surfaces in real life.	K ₂ , K ₃
DETAILED SYLLABUS		3-0-0
Unit	Topic	Proposed Lecture
I	Introduction and Line Generation: Types of computer graphics, Graphic Displays- Random scan displays, Raster scan displays, Frame buffer and video controller, Points and lines, Line drawing algorithms, Circle generating algorithms, Mid-point circle generating algorithm, and parallel version of these algorithms.	08
II	Transformations: Basic transformation, Matrix representations and homogenous coordinates, Composite transformations, Reflections and shearing. Windowing and Clipping: Viewing pipeline, Viewing transformations, 2-D Clipping algorithms- Line clipping algorithms such as Cohen Sutherland line clipping algorithm, Liang Barsky algorithm, Line clipping against non rectangular clip windows; Polygon clipping – Sutherland Hodgeman polygon clipping, Weiler and Atherton polygon clipping, Curve clipping, Text clipping	08
III	Three Dimensional: 3-D Geometric Primitives, 3-D Object representation, 3-D Transformation, 3-D viewing, projections, 3-D Clipping.	08
IV	Curves and Surfaces: Quadric surfaces, Spheres, Ellipsoid, Blobby objects, Introductory concepts of Spline, Bspline and Bezier curves and surfaces.	08
V	Hidden Lines and Surfaces: Back Face Detection algorithm, Depth buffer method, A- buffer method, Scan line method, basic illumination models– Ambient light, Diffuse reflection, Specular reflection and Phong model, Combined approach, Warn model, Intensity Attenuation, Color consideration, Transparency and Shadows.	08
Text books:		
<ol style="list-style-type: none"> 1. Donald Hearn and M Pauline Baker, “Computer Graphics C Version”, Pearson Education 2. Foley, Vandam, Feiner, Hughes – “Computer Graphics principle”, Pearson Education. 3. Rogers, “ Procedural Elements of Computer Graphics”, McGraw Hill 4. W. M. Newman, R. F. Sproull – “Principles of Interactive computer Graphics” – McGraw Hill. 5. Amrendra N Sinha and Arun D Udai,” Computer Graphics”, McGraw Hill. 6. R.K. Maurya, “Computer Graphics ” Wiley Dreamtech Publication. 7. Mukherjee, Fundamentals of Computer graphics & Multimedia, PHI Learning Private Limited. 8. Donald Hearn and M Pauline Baker, “Computer Graphics with Open GL”, Pearson education 		

ELECTRONICS AND COMPUTER ENGINEERING

Object Oriented System Design (KECZ-058)		
Course Outcome (CO)		Bloom's Knowledge Level (KL)
At the end of course , the student will be able to:		
CO 1	Understand the application development and analyze the insights of object oriented programming to implement application	K ₂ , K ₄
CO 2	Understand, analyze and apply the role of overall modeling concepts (i.e. System, structural)	K ₂ , K ₃
CO 3	Understand, analyze and apply oops concepts (i.e. abstraction, inheritance)	K ₂ , K ₃ , K ₄
CO 4	Understand the basic concepts of C++ to implement the object oriented concepts	K ₂ , K ₃
CO 5	To understand the object oriented approach to implement real world problem.	K ₂ , K ₃
DETAILED SYLLABUS		3-0-0
Unit	Topic	Proposed Lecture
I	Introduction: The meaning of Object Orientation, object identity, Encapsulation, information hiding, polymorphism, generosity, importance of modelling, principles of modelling, object oriented modelling, Introduction to UML, conceptual model of the UML, Architecture.	08
II	Basic Structural Modeling: Classes, Relationships, common Mechanisms, and diagrams. Class & Object Diagrams: Terms, concepts, modelling techniques for Class & Object Diagrams. Collaboration Diagrams: Terms, Concepts, depicting a message, polymorphism in collaboration Diagrams, iterated messages, use of self in messages. Sequence Diagrams: Terms, concepts, depicting asynchronous messages with/without priority, call-back mechanism, broadcast messages. Basic Behavioural Modeling: Use cases, Use case Diagrams, Activity Diagrams, State Machine , Process and thread, Event and signals, Time diagram, interaction diagram, Package diagram. Architectural Modeling: Component, Deployment, Component diagrams and Deployment diagrams.	08
III	Object Oriented Analysis: Object oriented design, Object design, Combining three models, Designing algorithms, design optimization, Implementation of control, Adjustment of inheritance, Object representation, Physical packaging, Documenting design considerations. Structured analysis and structured design (SA/SD), Jackson Structured Development (JSD). Mapping object oriented concepts using non-object oriented language, Translating classes into data structures, Passing arguments to methods, Implementing inheritance, associations encapsulation. Object oriented programming style: reusability, extensibility, robustness, programming in the large. Procedural v/s OOP, Object oriented language features. Abstraction and Encapsulation.	08
IV	C++ Basics : Overview, Program structure, namespace, identifiers, variables, constants, enum, operators, typecasting, control structures C++ Functions : Simple functions, Call and Return by reference, Inline functions, Macro Vs. Inline functions, Overloading of functions, default arguments, friend functions, virtual functions	08
V	Objects and Classes : Basics of object and class in C++, Private and public members, static data and function members, constructors and their types, destructors, operator overloading, type conversion. Inheritance : Concept of Inheritance, types of inheritance: single, multiple, multilevel, hierarchical, hybrid, protected members, overriding, virtual base class Polymorphism : Pointers in C++, Pointes and Objects, this pointer, virtual and pure virtual functions, Implementing polymorphism	08
Text Books		
<ol style="list-style-type: none"> 1. James Rumbaugh et. al, "Object Oriented Modeling and Design", Pearson Education 2. Grady Booch, James Rumbaugh, Ivar Jacobson, "The Unified Modeling Language User Guide", Pearson Education 3. Object Oriented Programming With C++, E Balagurusamy, McGraw Hill. 4. C++ Programming, Black Book, Steven Holzner, dreamtech 5. Object Oriented Programming in Turbo C++, Robert Lafore, Galgotia 6. Object Oriented Programming with ANSI and Turbo C++, Ashok Kamthane, Pearson 7. The Compete Reference C++, Herbert Schlitz, McGraw Hill. 		

KECZ-551	INTEGRATED CIRCUITS LAB	0L:0T:2P	1 Credit
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SUGGESTIVE LIST OF EXPERIMENTS:

1. Design the following using Op-Amp: (*Through Virtual Lab Link 1*)
 - a) A unity gain amplifier.
 - b) An inverting amplifier with a gain of “A”.
 - c) A non-inverting amplifier with a gain of “A”
2. Study and design Log and antilog amplifiers.
3. Voltage to current and current to voltage convertors.
4. Second order filters using operational amplifier for: (*Through Virtual Lab Link 1*)
 - a) Low pass filter of cutoff frequency 1 KHz.
 - b) High pass filter of frequency 12 KHz.
5. Realization of Band pass filter with unit gain of pass band from 1 KHz to 12 KHz.
6. Study and design voltage comparator and zero crossing detectors.
7. Function generator using operational amplifier (sine, triangular & square wave).
8. Design and construct astable multivibrator using IC 555 and
 - a) Plot the output waveform
 - b) Measure the frequency of oscillation (*Through Virtual Lab Link 2*)
9. Design and construct a monostable multivibrator using IC 555 and
 - a) Plot the output waveform
 - b) Measure the time delay (*Through Virtual Lab Link 2*)
10. Implement Schmitt Trigger Circuit using IC 555. (*Through Virtual Lab Link 2*)
11. Implement voltage-controlled oscillator using IC566 and plot the waveform. (*Through Virtual Lab Link 2*)
12. Study and design ramp generator using IC 566.

Virtual Lab Link:

1. <http://vlabs.iitkgp.ernet.in/be/exp17/index.html>
2. <http://hecoep.vlabs.ac.in/Experiment8/Theory.html?domain=ElectronicsandCommunications&lab=Hybrid%20Electronics%20Lab>

Available on: <http://www.vlab.co.in/broad-area-electronics-and-communications>

Course Outcomes:

At the end of this course students will demonstrate the ability to:

1. Design different non-linear applications of operational amplifiers such as log, antilog amplifiers and voltage comparators.
2. Explain and design different linear applications of operational amplifiers such as filters.
3. Demonstrate the function of waveforms generator using op-Amp.
4. Construct multivibrator and oscillator circuits using IC555 and IC566 and perform measurements of frequency and time.
5. Design and practically demonstrate the applications based on IC555 and IC566.

SUGGESTIVE LIST OF EXPERIMENTS:

1. Introduction to MATLAB and or Open Source Software, Scilab (Using Spoken Tutorial MOOCs).
2. Write a Program for the generation of basic signals such as unit impulse, unit step, ramp, exponential, sinusoidal and cosine.
3. Implement IIR Butterworth analog Low Pass for a 4 KHz cut off frequency.
4. Verify Blackman and Hamming windowing techniques.
5. Evaluate 4-point DFT of and IDFT of $x(n) = 1, 0 \leq n \leq 3; 0$ elsewhere.
6. Verify Linear convolution of two sequences using FFT
7. Verify Circular Convolution of two sequences using FFT.
8. To verify FFT as sample interpolator.
9. To implement Tone Generation.
10. To implement floating point arithmetic.
11. To study about DSP Processors and architecture of TMS320C6713 DSP processor.
12. **VIRTUAL Lab by NME-ICT available at: (*Through Virtual Lab*)**
 - 12.1 Study of Discrete Fourier Transform (DFT) and its inverse.
 - 12.2 Study of FIR filter design using window method: Lowpass and highpass filter.
 - 12.3 Study of FIR filter design using window method: Bandpass and Bandstop filter.
 - 12.4 Study of Infinite Impulse Response (IIR) filter.

Virtual Lab Link: <http://vlabs.iitkgp.ernet.in/dsp/index.html#>
<http://vlabs.iitkgp.ernet.in/dsp/>

Available on: <http://www.vlab.co.in/broad-area-electronics-and-communications>

Spoken Tutorial (MOOCs):

Spoken Tutorial MOOCs, ' Course on Scilab', IIT Bombay (<http://spoken-tutorial.org/>)

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Create and visualize various discrete/digital signals using MATLAB/Scilab.
2. Implement and test the basic operations of Signal processing.
3. Examine and analyse the spectral parameters of window functions.
4. Design IIR and FIR filters for band pass, band stop, low pass and high pass filters.
5. Design the signal processing algorithms using MATLAB/Scilab.

ELECTRONICS AND COMPUTER ENGINEERING

Database Management Systems Lab (KECZ-553)		
	Course Outcome (CO)	Bloom's Knowledge Level (KL)
At the end of course , the student will be able to:		
CO 1	Understand and apply oracle 11 g products for creating tables, views, indexes, sequences and other database objects.	K2, K4
CO 2	Design and implement a database schema for company data base, banking data base, library information system, payroll processing system, student information system.	K3, K5, K6
CO 3	Write and execute simple and complex queries using DDL, DML, DCL and TCL	K4, K5
CO 4	Write and execute PL/SQL blocks, procedure functions, packages and triggers, cursors.	K4, K5
CO 5	Enforce entity integrity, referential integrity, key constraints, and domain constraints on database.	K3, K4
DETAILED SYLLABUS		
<ol style="list-style-type: none"> 1. Installing oracle/ MYSQL 2. Creating Entity-Relationship Diagram using case tools. 3. Writing SQL statements Using ORACLE <ul style="list-style-type: none"> /MYSQL: a)Writing basic SQL SELECT statements. b) Restricting and sorting data. c)Displaying data from multiple tables. d)Aggregating data using group function. e)Manipulating data. e)Creating and managing tables. 4. Normalization 5. Creating cursor 6. Creating procedure and functions 7. Creating packages and triggers 8. Design and implementation of payroll processing system 9. Design and implementation of Library Information System 10. Design and implementation of Student Information System 11. Automatic Backup of Files and Recovery of Files 12. Mini project (Design & Development of Data and Application) for following : <ul style="list-style-type: none"> a) Inventory Control System. b) Material Requirement Processing. c) Hospital Management System. d) Railway Reservation System. e) Personal Information System. f) Web Based User Identification System. g) Timetable Management System. h) Hotel Management System 		
<p>Note: The Instructor may add/delete/modify/tune experiments, wherever he/she feels in a justified manner It is also suggested that open source tools should be preferred to conduct the lab (MySQL , SQL server , Oracle ,MongoDB ,Cubrid ,MariaDBetc)</p>		